

**AMENDMENTS TO THE CLAIMS:**

This listing of claims will replace all prior versions, and listings, of claims in the application:

**LISTING OF CLAIMS:**

1. (Currently Amended) Process of controlling the execution of a computer program, comprising the following steps ~~consisting of~~:
  - (1) Splitting a program into at least two parts, respectively public and secret, the public part being suitable for execution on a first processing means, and the secret part being suitable for execution on a secure, second processing means;
  - (2) Placing the public part in a memory of the first processing means.
  - (3) ~~Placing~~ Storing the secret part on a non-volatile secure medium of the a portable second processing means ~~intended to comprising a chip medium that can~~ be connected to and detached from the first processing means;
  - (4) Carrying out the following operations for the execution of the program by the first processing means:
    - (a) connecting the second processing means to the first, and transmission, from the first processing means to the second, of parameters/variables which are functions of external signals initiated by a user,
    - (b) execution of at least a portion of the program by the second processing means, ~~putting into effect a certain number of the~~ utilizing said received parameters/variables,
    - (c) transmission of the results of the execution of the preceding paragraph (b) from the second processing means to the first, and

(d) using ~~a certain number of the~~ said results in the execution effected by the first means, ~~wherein the second means is a portable and detachable accessory chip medium.~~

2. (Currently Amended) Process of controlling the execution of a computer program, comprising the following steps ~~consisting of~~:

(1) Splitting a program into at least two parts, respectively public and secret, the public part being suitable for execution on a first processing means, and the secret part being suitable for execution on a secure, second processing means;

(2) Encoding at least a the secret part and placing it with the public part on the same medium, the latter being ~~intended to be connected~~ connectable to the first processing means;

(3) Placing a corresponding decoding function on the second processing means comprising a portable chip medium that is connectable to and detachable from said first processing means;

(4) Carrying out the following operations for the execution of the program:

(a) connecting the second processing means to the first, and transmission, from the first processing means to the second, of all or a portion of the encoded secret part,

(b) decoding ~~the~~ said encoded secret part received by the second, secure processing means by making use of ~~the~~ said decoding function, and storing the decoded secret part in secure memory,

(c) transmission from the first processing means to the second of parameters/variables which are functions of external signals,

(d) execution of at least a said secret ~~portion~~ part by the second, secure processing means, using ~~a certain number of the~~ said received parameters/variables,

(e) transmission of the results of the execution of the preceding paragraph (d) from the second processing means to the first, and

(f) using ~~a certain number of the~~ said results in the execution effected by the first means[[:]]

~~wherein the second means is a portable and detachable accessory chip~~  
medium.

3. (Original) Process according to claim 2, wherein, in the operation (a), a portion of the encoded program is transmitted to the extent needed and/or as a function of the capacity of the second, secure processing means.

4. (Previously Presented) Process according to claim 1, wherein the second processing means is a card having a microprocessor.

5. (Previously Presented) Process according to claim 1, wherein the second processing means is in a hardwired form on a memory card.

6. (Previously Presented) Process according to claim 1, wherein the first processing means is a central processing unit of a computer.

7. (Currently Amended) Process according to claim 6, wherein the central processing unit is connected to a network, ~~particularly of the Internet type~~, on which at least the public part of the program is available on demand.

8. (Currently Amended) Process according to claim 2, wherein a secure distribution is effected of utilization rights of the said program to a medium of a user via a server.

9. (Currently Amended) Process according to claim 8, ~~characterized in that the support~~ wherein said medium sends to a server a request for loading rights, containing the identity of the program and an identity of the medium, the said server combines an identity of the requester's medium with a rights encoding key, the result being a number of bits suitable for use as a diversified encoding key, the server uses this diversified key to encode the requested rights, and sends the thus encoded rights to the requester's medium.

10. (New) Process according to claim 2, wherein the second processing means is a card having a microprocessor.

11. (New) Process according to claim 2, wherein the second processing means is in a hardwired form on a memory card.

12. (New) Process according to claim 2, wherein the first processing means is a central processing unit of a computer.